IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently Amended) A Line driver arrangement comprising:

(a) a differential class-D switching amplifier having a switching frequency, said differential class-D amplifier receiving [[an]] a dual line input transmit signal and outputting an amplified dual line transmit signal; and

(b) a transformer having a predetermined leakage inductance, having a primary winding for receiving the amplified dual line transmit signal, and having a secondary winding for [[and]]] outputting a transformed dual line signal as [[an]] a dual line output transmit signal;

wherein the leakage inductance is predetermined for low pass filtering of the amplified dual line transmit signal.

2. (Original) Line driver arrangement according to claim 1, wherein the leakage inductance is predetermined to minimize a resonance at a resonance frequency in the power spectral density of the line driver arrangement, wherein the resonance is caused by the switching frequency of the class-D amplifier.

3. (Cancelled)

4. (Currently Amended) Line driver arrangement according to claim [[3]] 1, wherein the dual line signals are ADSL signals being discrete multitone modulated signals.

5. (Original) Line driver arrangement according to claim 1, wherein at least one capacitance is connected between the two lines between the class-D amplifier and the transformer.

6. (Original) Line driver arrangement according to claim 1, wherein two capacitances are connected in series between the two lines between the class-D amplifier and the transformer, and wherein a node between the two capacitances is connected to a reference voltage.

7. (Previously Presented) Line driver arrangement according to claim 5, wherein the leakage inductance and the capacitances form a low pass filter having a cutoff frequency that is lower than a resonance frequency.

8. (Original) Line driver arrangement according to claim 1, wherein the transformer further has a stray capacitance that is predetermined to minimize the resonance in the power spectral density of the line driver arrangement.

9. (Original) Line driver arrangement according to claim 1, wherein a low pass filter is coupled between the class-D amplifier and the transformer.

10. (Original) Line driver arrangement according to claim 1, wherein the line driver has a power spectral density that complies with an ADSL and/or ADSL+ standard.

11. (Original) Line driver arrangement according to claim 1, wherein the line driver is part of an ADSL transceiver.

12. (Original) Line driver arrangement according to claim 1, wherein the line driver arrangement further comprises resistances and/or inductances.

13. (Currently Amended) Transformer for use in a line driver arrangement, said line driver arrangement comprising [[an]] a differential amplifier for receiving [[an]] a dual line input transmit signal and outputting an amplified dual line transmit signal, [[and]] wherein the transformer comprises a primary winding for receiving the amplified dual line transmit signal and a secondary winding for outputting a transformed dual line signal as a dual line output transmit signal, said transformer having [[has]] a predetermined leakage inductance and/or stray capacitance, [[and]] wherein the leakage inductance and/or stray capacitance is predetermined for low pass filtering of the amplified dual line transmit signal.

14. (Original) Transformer according to claim 13, wherein the amplifier is a class-D switching amplifier having a switching frequency.